


PATENT

H1907/AMDP986US

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I hereby certify that this correspondence (along with any paper referred to as being attached or enclosed) is being faxed to 571-273-8300 on the date shown below to Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Date: 12-8-05
Christina M. Padamonsky**RECEIVED
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Applicant(s): Khoi A. Phan, *et al.*

Examiner: Paul Dinh

Serial No: 10/790,296

Art Unit: 2825

Filing Date: March 1, 2004

Title: MULTI-LAYER OVERLAY MEASUREMENT AND CORRECTION TECHNIQUE
FOR IC MANUFACTURING

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

SUBMISSION PURSUANT TO 37 CFR §1.114

Dear Sir:

This submission is being concurrently filed with an RCE, pursuant to 37 CFR §1.114, for the above-identified patent application. Favorable consideration of the subject patent application is respectfully requested in view of the comments and amendments herein. A credit card payment form is filed concurrently herewith in connection with all fees due regarding this document and the RCE. In the event any additional fees may be due and/or are not covered by the credit card, the Commissioner is authorized to charge such fees to Deposit Account No. 50-1063 [AMDP986US].